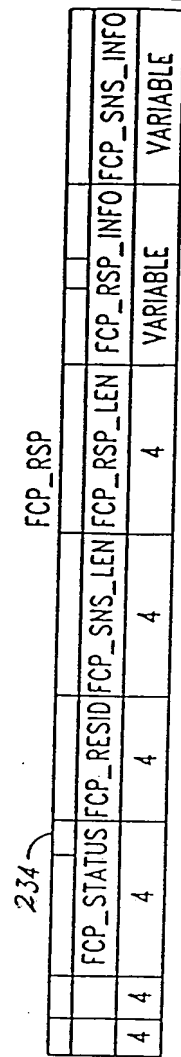
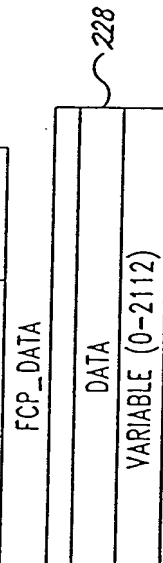
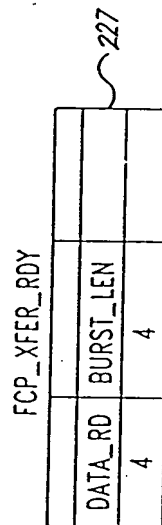
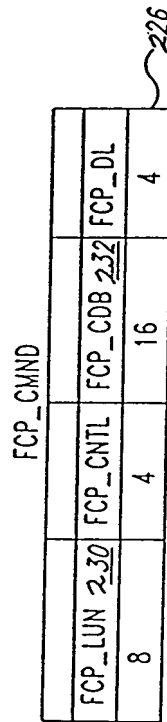
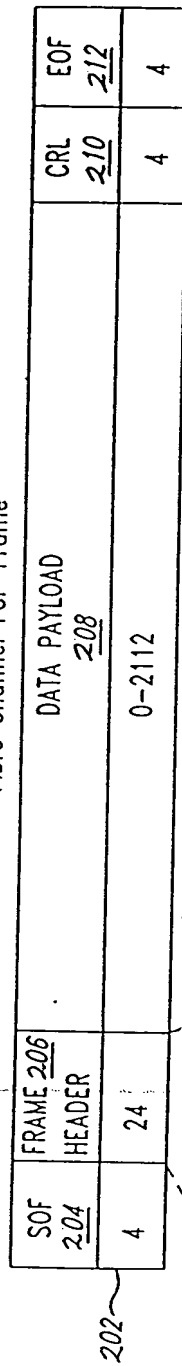


*Fig. 1*

Fibre Channel FCP Frame



0 1 2 3

Fig. 2

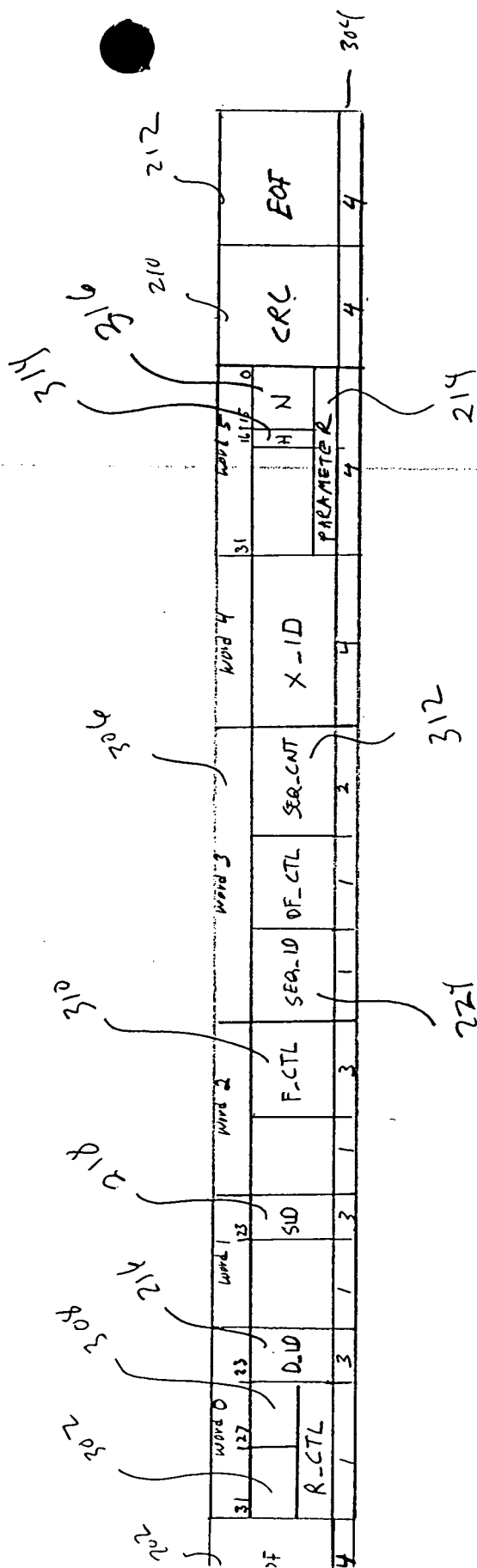
[illegible]

FIGURE 3

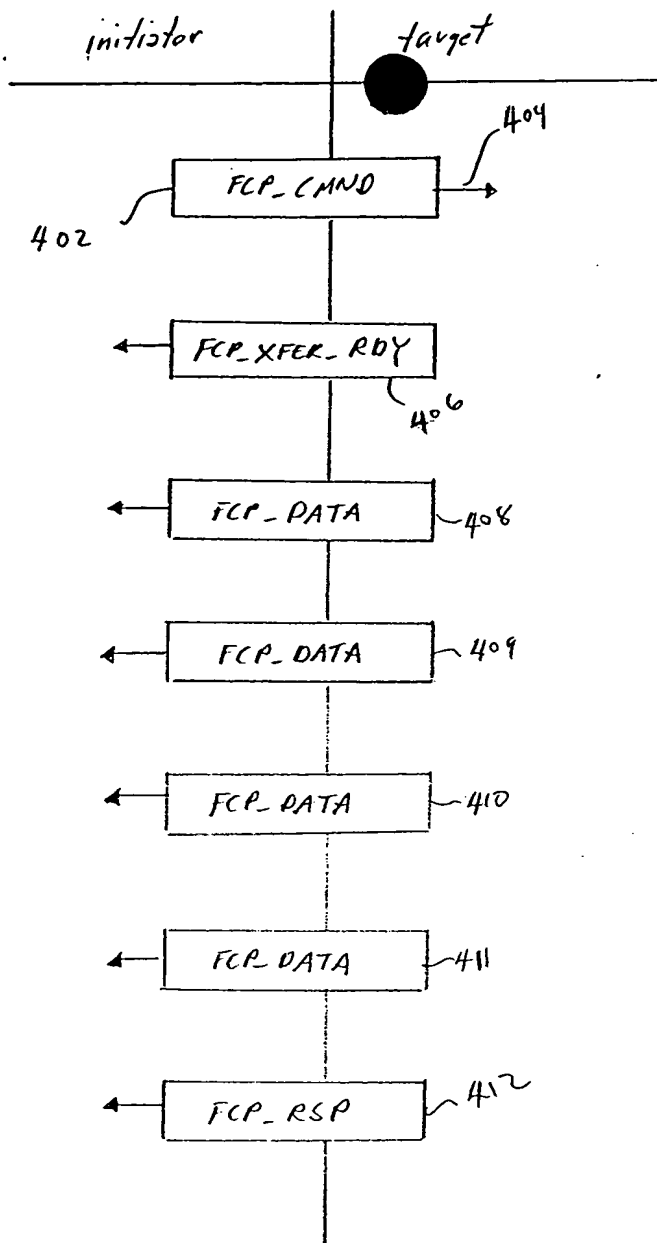


Figure 4A

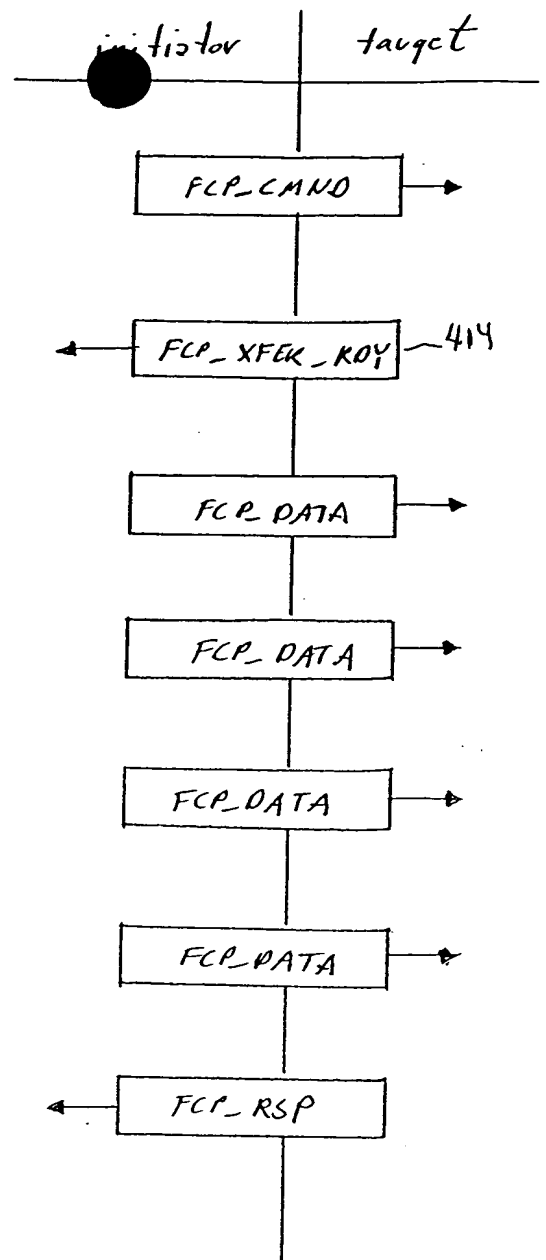


Figure 4B

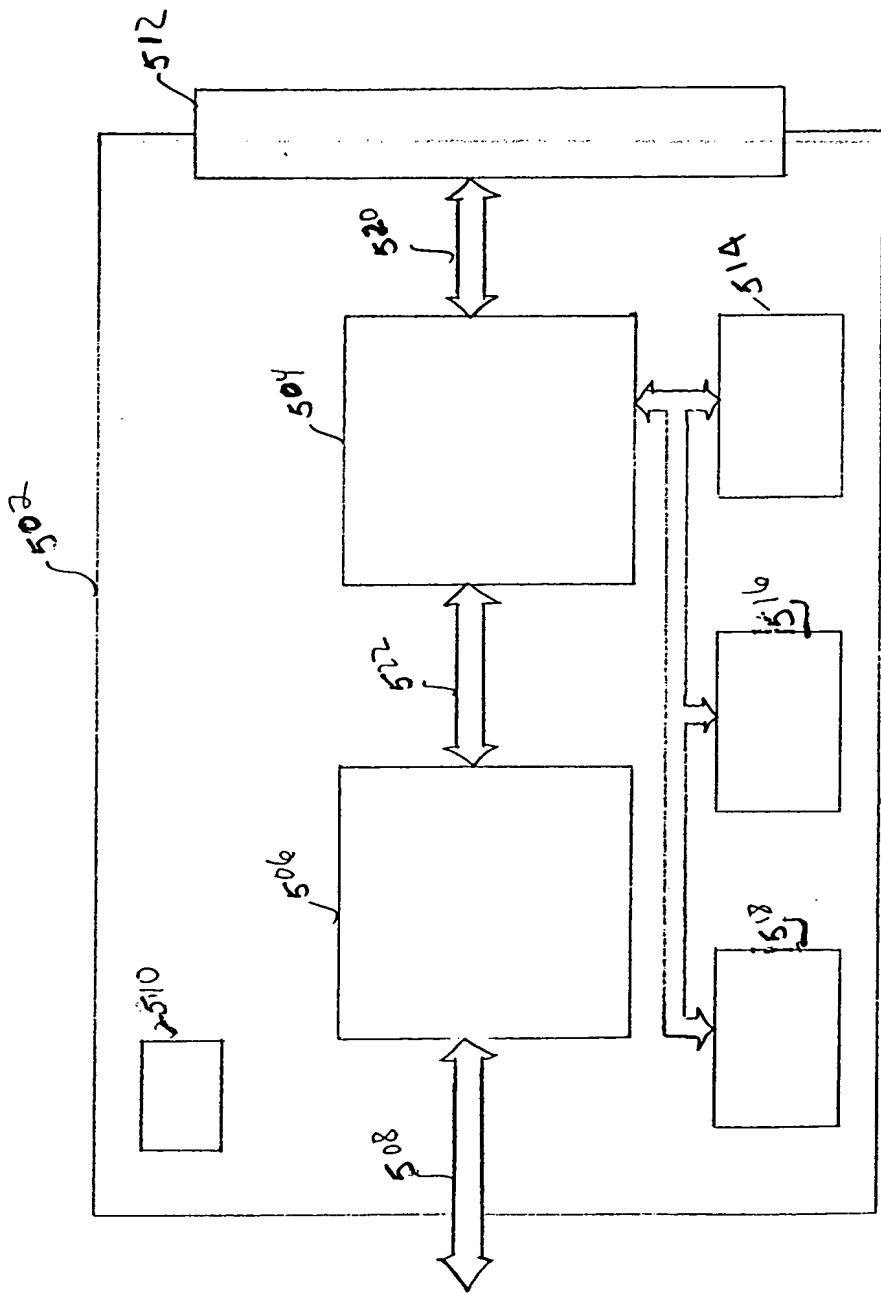


Figure 5

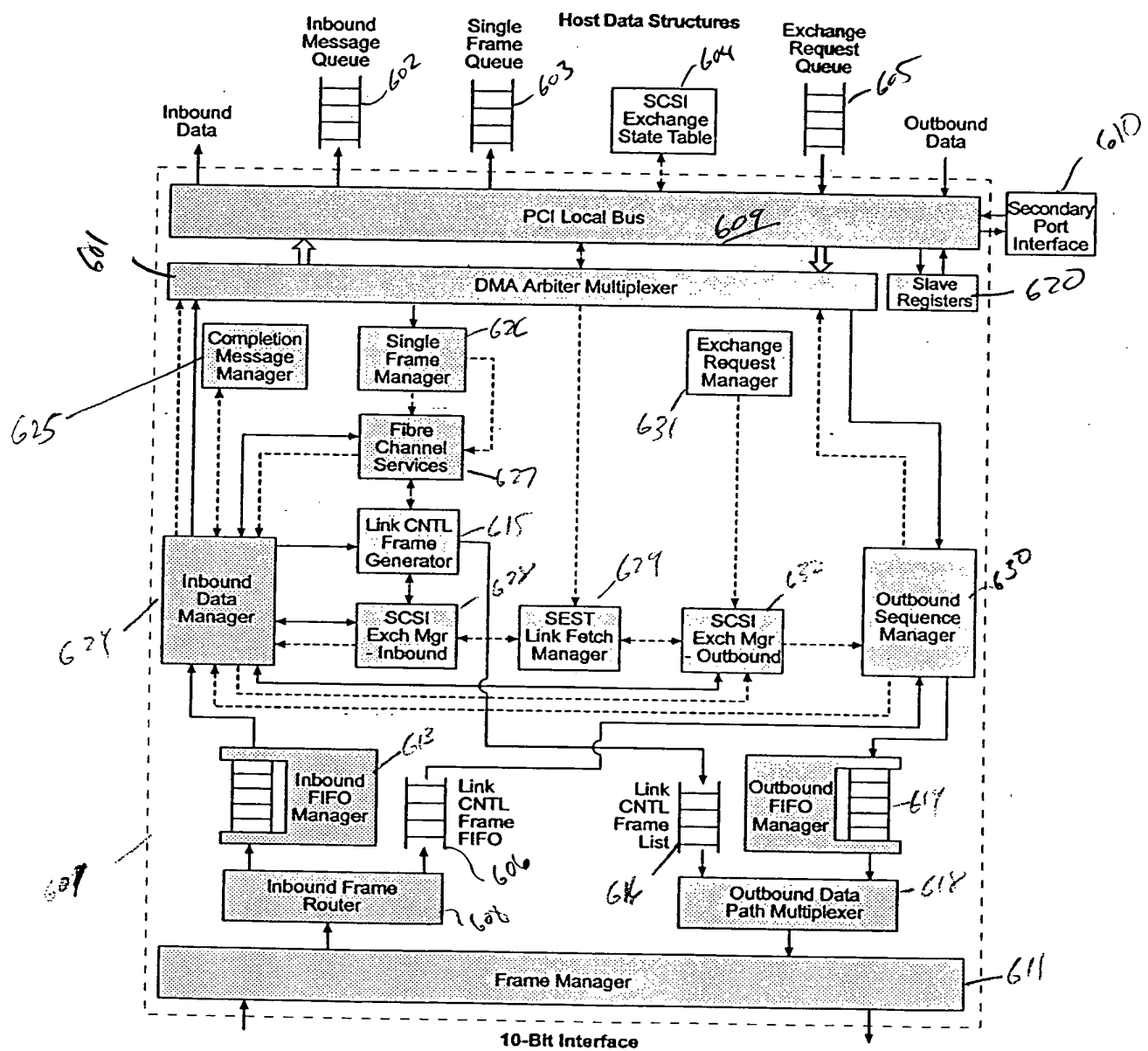


Figure 6

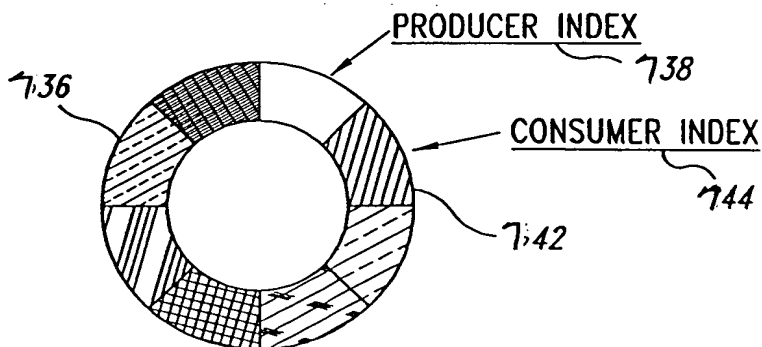
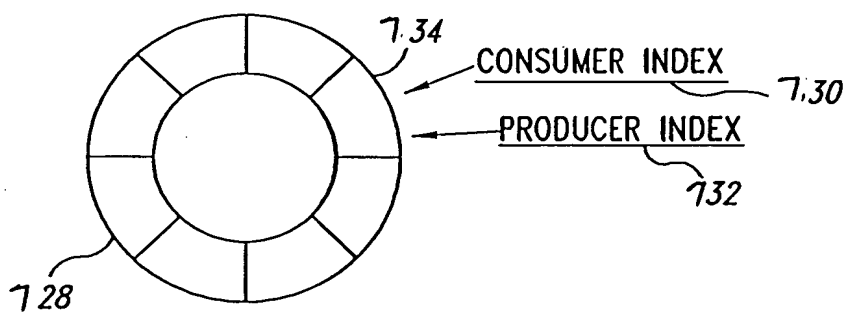
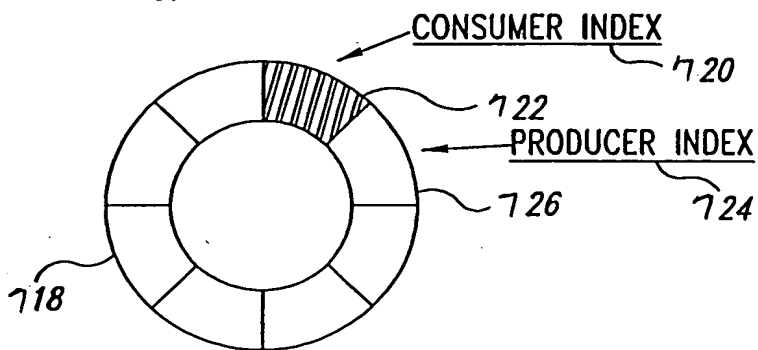
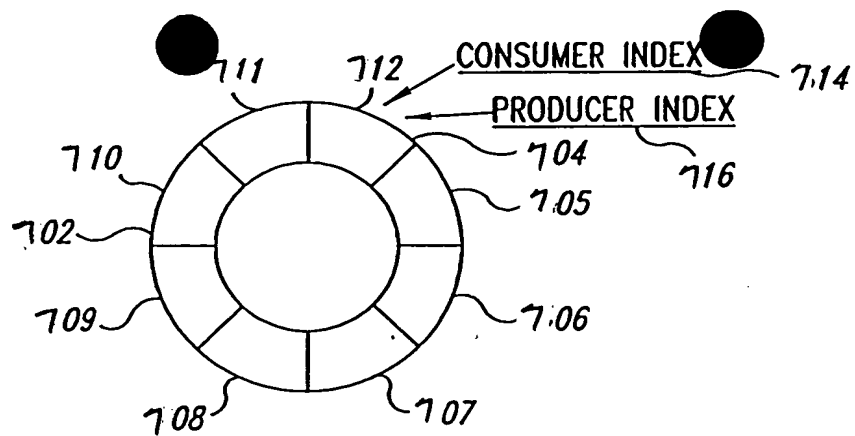


Fig. 7

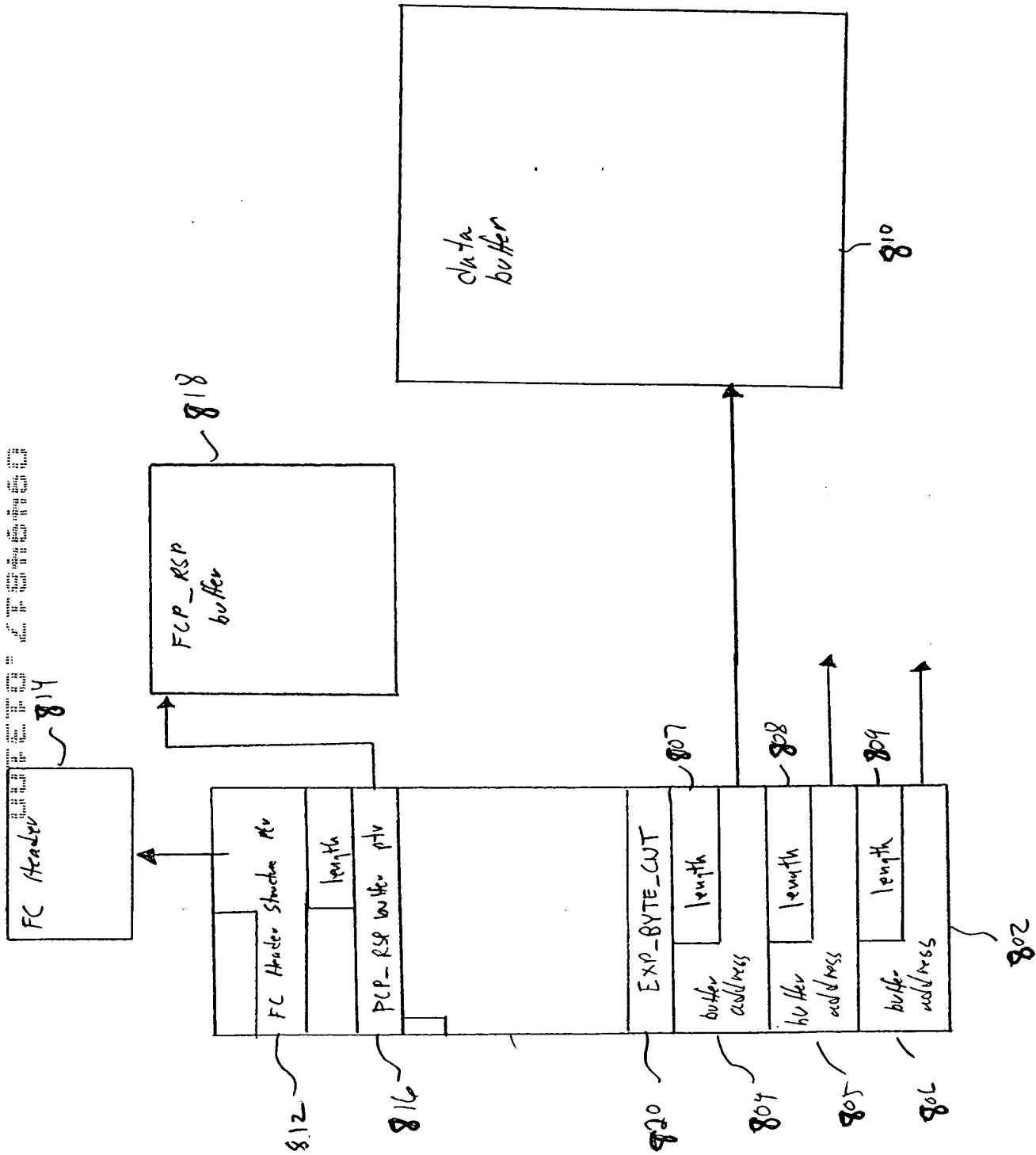


Figure 8



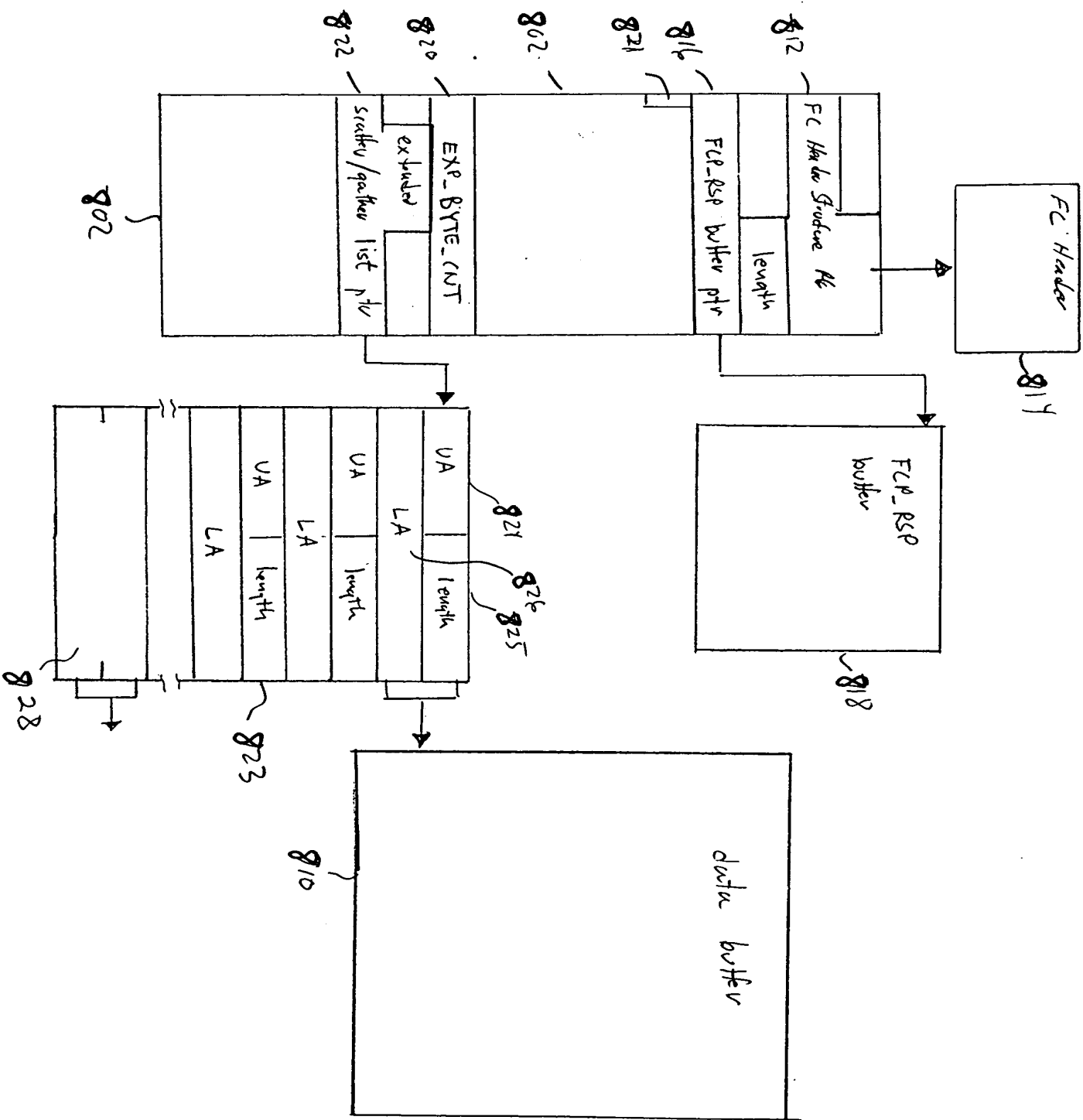


Figure 8B



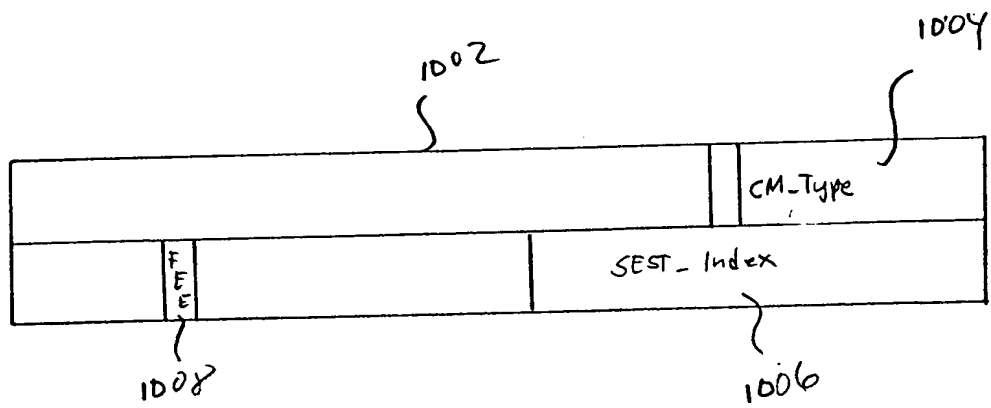


Figure 10

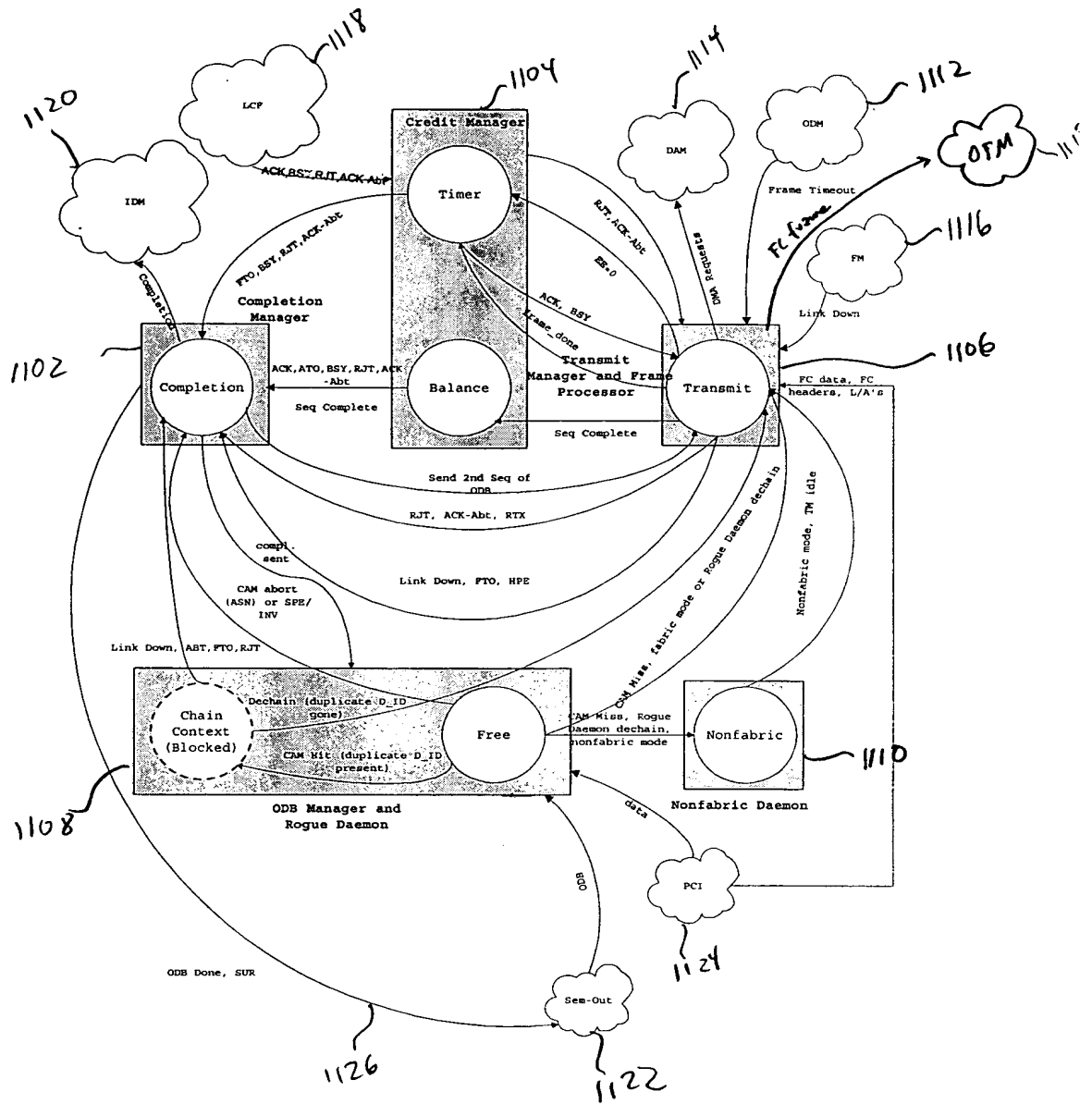


Figure 11

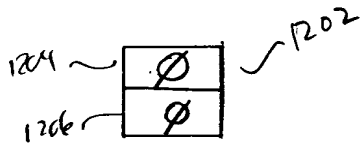


Figure 12A

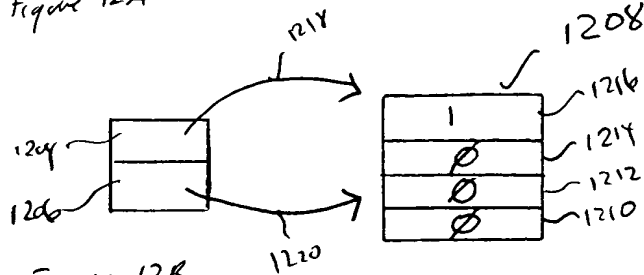


Figure 12B

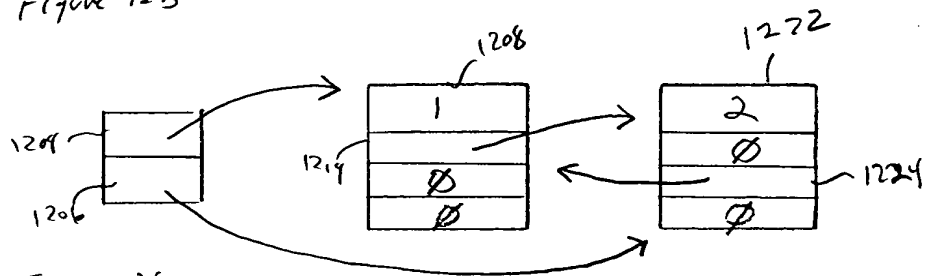


Figure 12C

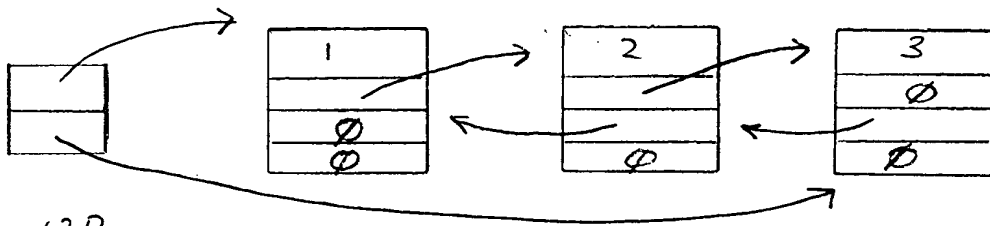


Figure 12D

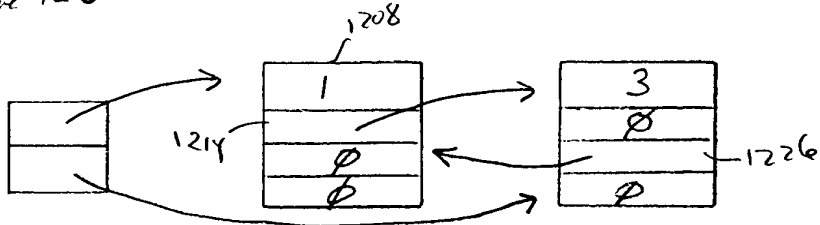


Figure 12E

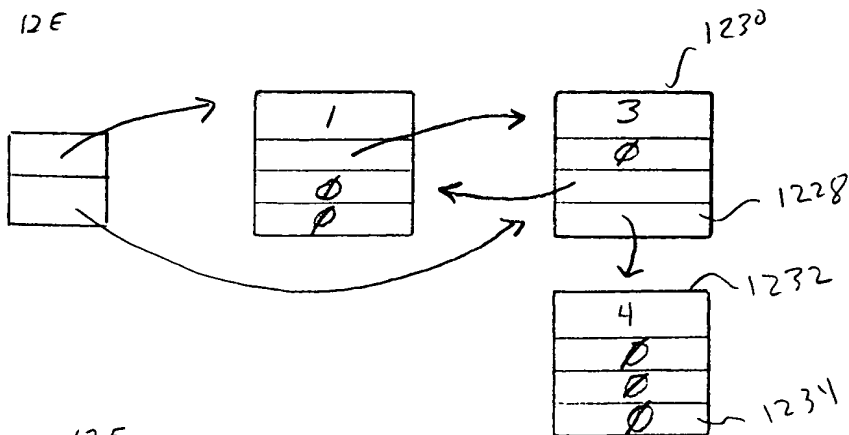


Figure 12F

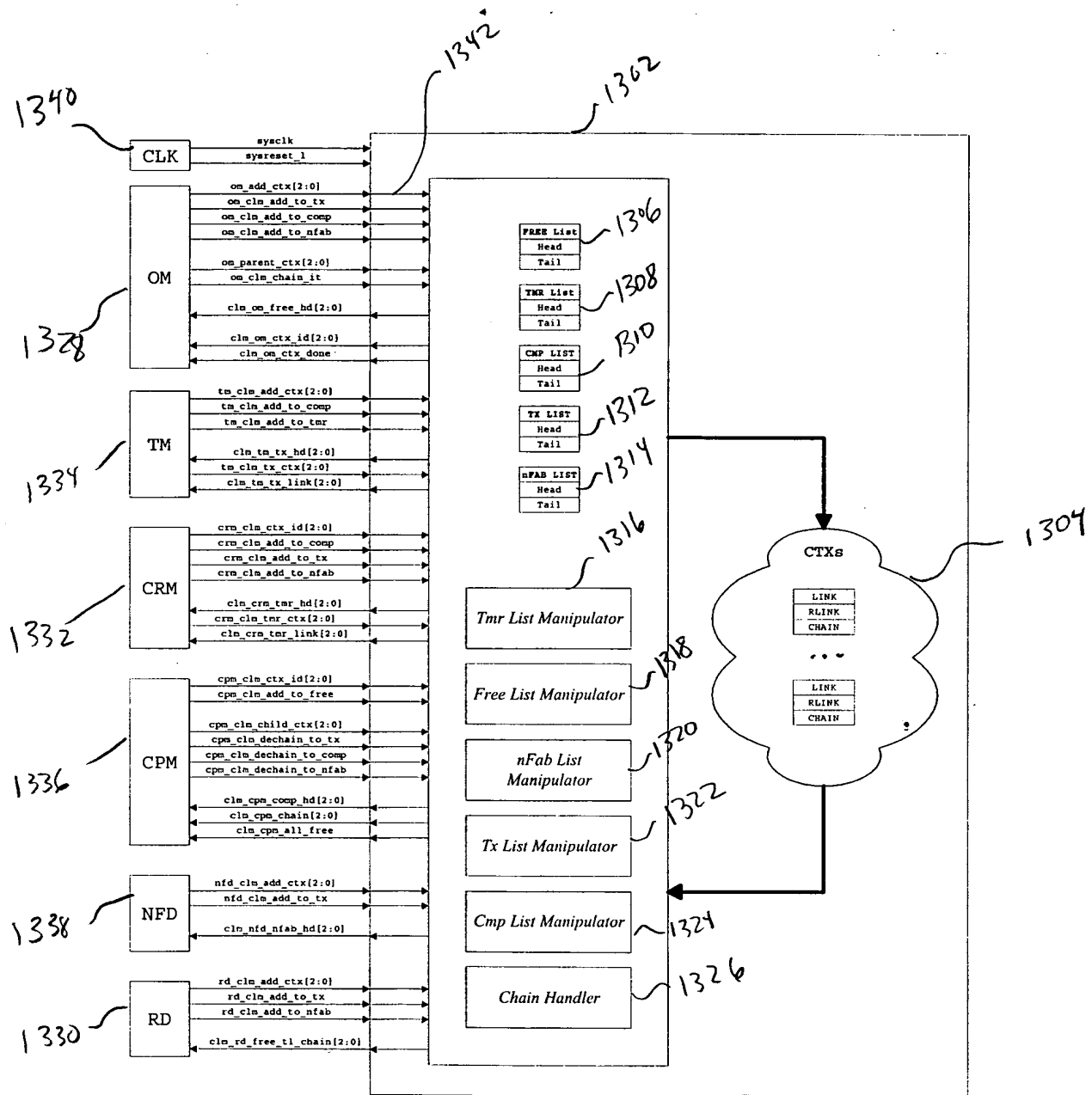


Figure 13

Figure 14A

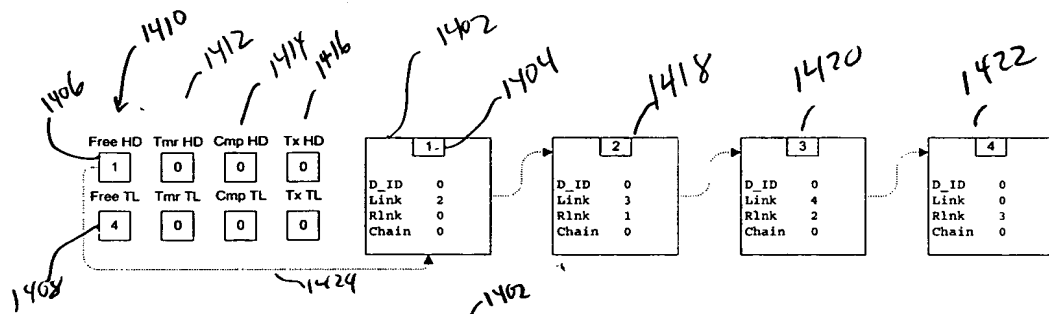


Figure 14B

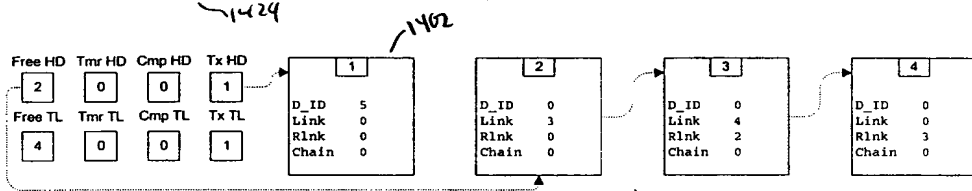


Figure 14C

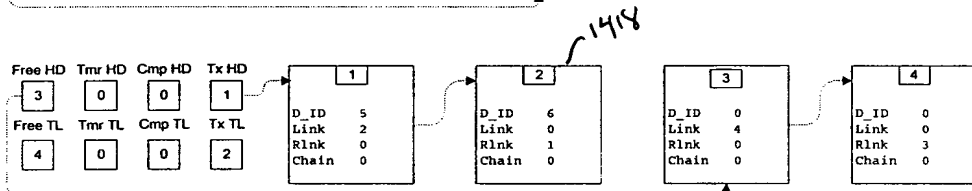


Figure 14D

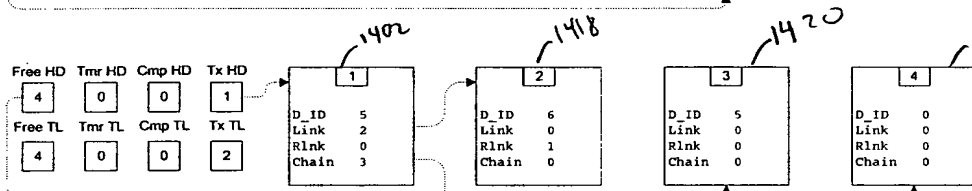


Figure 14E

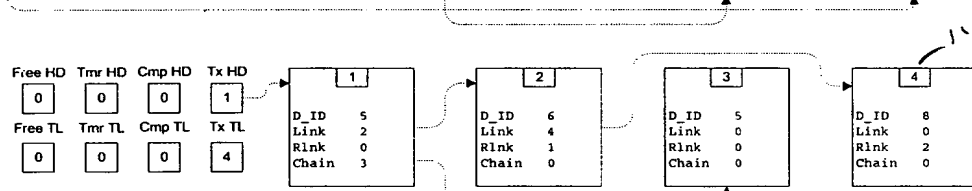
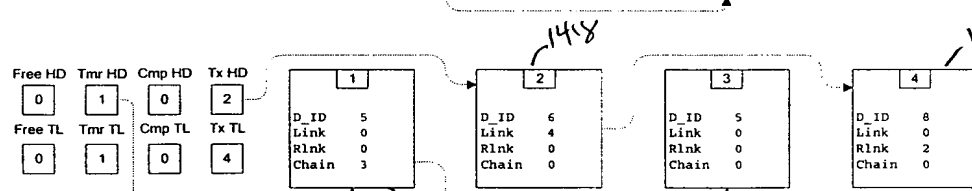


Figure 14F



Comments  
Context 1 complete  
Context 1 on Timer  
List to wait for  
credit.

Figure 14G

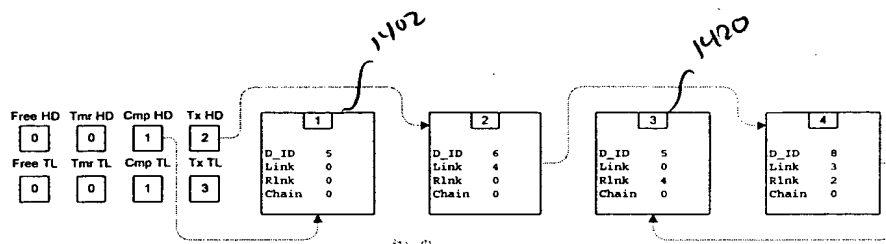


Figure 14H

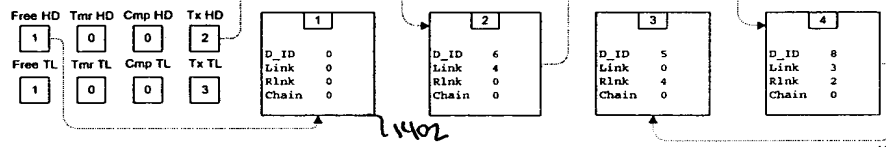


Figure 14I

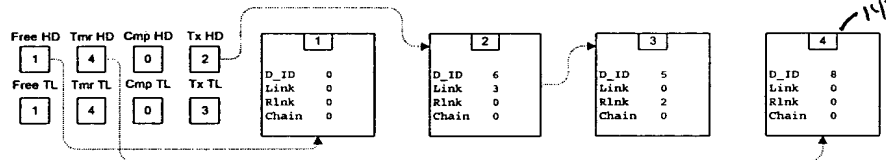


Figure 14J

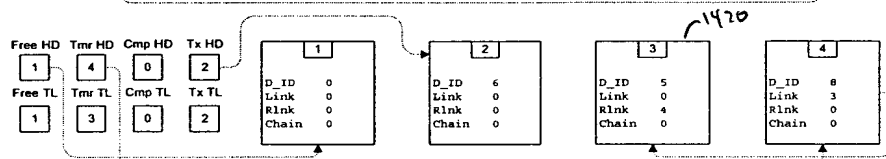


Figure 14K

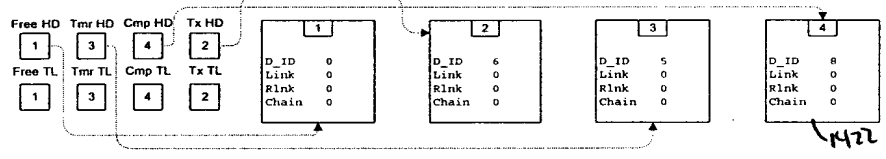


Figure 14L

